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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

of 1 Sheet

Substitute for form 1449A/PTO

Co.	mplete if Known
Application Number	Not yet assigned
Filing Date	Filed concurrently herewith
First Named Inventor	Timothy M. Takeuchi
Art Unit	Not yet assigned
Examiner Name	Not yet assigned
Attorney Docket Number	42390P13557

U.S. PATENT DOCUMENTS					
Examiner Initials	Cite No. 1 Number - Kind Code 2 (if known	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
QUU	us- 5,786,630	07-28-1998	Bhansali et al.		
avu	US- 5,886,406	03-23-1999	Bhansali		
avu	US- 5,990,552	11-23-1999	Xie et al.		
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		FORE	IGN PATENT D	OCUMENTS		
Examiner Initials		Foreign Patent Document Country Code 3 - Number 4 - Kind Code 5 (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	Т6
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Examiner		Date	odlalos	
Signature	Cuuu	Considered	08/19/02	

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Substitute for form 1449B/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT		Complete if Known				
		01 001105	Application Number	Not yet assigned		
INFOR	KMATION L	TION DISCLOSURE NT BY APPLICANT Filing Date First Named Inventor Group Art Unit Mot yet assigned Timothy M. Takeuchi Not yet assigned Not yet assigned Not yet assigned Not yet assigned				
STATEMENT BY APPLICANT		First Named Inventor	Timothy M. Takeuchi			
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_	(use as many shee	ts as	s necessary)	Examiner Name	Not yet assigned	
Sheet	2	of	2	Attorney Docket Number	42390P13557	

Examiner Initials	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T
		MIRNGH-JI LII ET AL.; Flip-Chip Technology on Organic Pin Grid Array Packages; Intel Technology Journal Q3, 2000; pp. 1-9.	
QVU		RAVI MAHAJAN ET AL.; The Evolution of Microprocessor Packaging; Intel Technology Journal Q3, 2000; pp.1-10.	

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